

Abstract of the Disclosure:

An integrated semiconductor circuit having a number of circuit units which are driven by a clock signal and can be operated both in parallel and in series is provided. A connection
5 supplying the clock signal is connected to the clock input of the respective circuit units via respective controllable switching devices. The control inputs of the switching devices are connected to an output of a random signal generator, so that a circuit unit is operated in parallel or in series with
10 one or more of the other circuit units on the basis of the random signal. A method of operating an integrated semiconductor circuit is also provided.

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